PetaBricks

A Language and Compiler for Algorithmic Choice

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MIT - CSAIL

June 16, 2009

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Outline

Introduction

Motivating Example

- Language & Compiler Overview
- Why choices

PetaBricks Language

- Key Ideas
- Compilation Example
- Other Language Features
- 3 Results
 - Benchmarks
 - Scalability
 - Variable Accuracy
- Conclusion
 - Final thoughts

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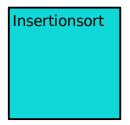
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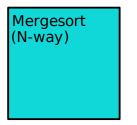
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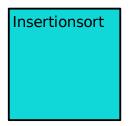




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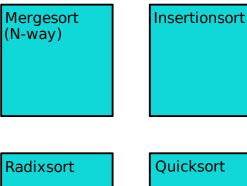






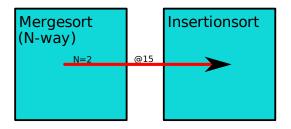
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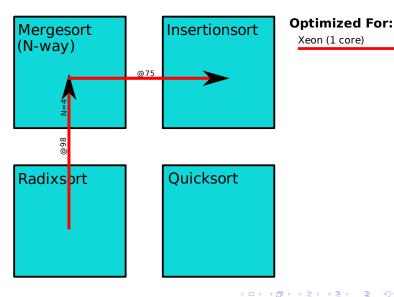


STL Algorithm

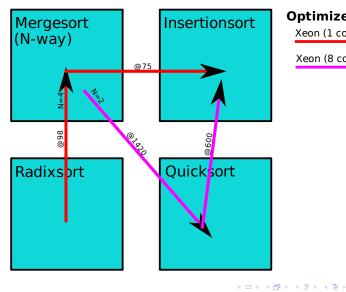




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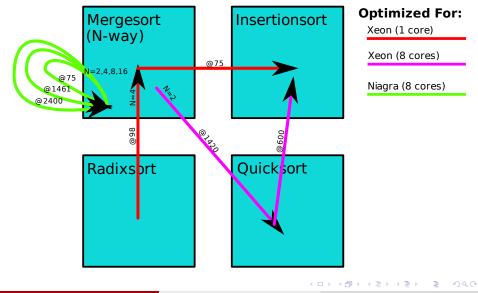
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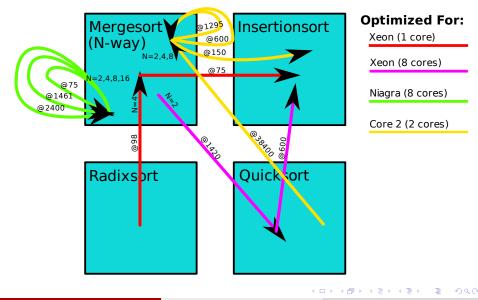
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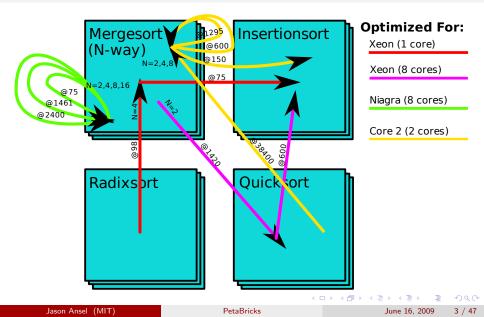
Xeon (1 core)

Xeon (8 cores)



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• The case for autotuning is obvious

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- How should the programmer represent choice?

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- We present the **PetaBricks** programming language and compiler:

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- We present the **PetaBricks** programming language and compiler:
 - Choice as a fundamental language construct
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 - Automatically parallelized

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transform Sort
1
2 from A[n]
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3
4
   {
5
      from(A a) to(B b) {
6
        tunable WAYS:
7
        /* Mergesort */
8
      } or {
9
        /* Insertionsort */
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 Jason Ansel (MIT)
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Image: A match a ma

- Sort is compiled into a autotuning binary
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Image: A matrix A

- Sort is compiled into a autotuning binary
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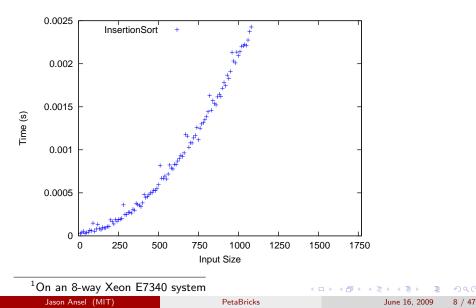
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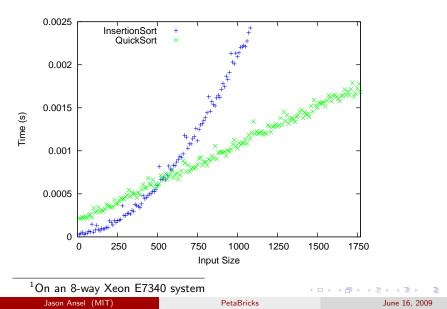
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- Trained on target architecture
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 - Trained with full number of threads
 - Under 1 minute for Sort
- Results fed back into the compiler
- Final binary created

Sort algorithm timings¹



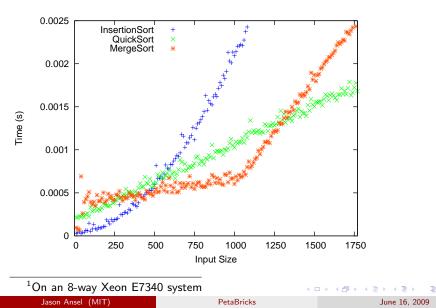
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Sort algorithm timings¹



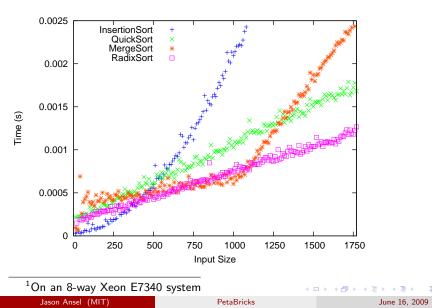
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Sort algorithm timings¹

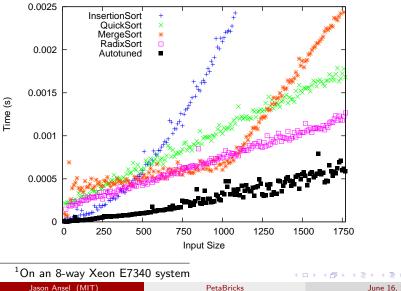


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Sort algorithm timings¹



Sort algorithm timings¹



June 16, 2009

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Timings on different architectures

		Trained on				
		Mobile	Xeon 1-way	Xeon 8-way	Niagara	
Run on	Mobile	-	1.09×	1.67×	1.47x	
	Xeon 1-way	1.61x	-	2.08×	2.50x	
	Xeon 8-way	1.59x	2.14x	-	2.35x	
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Early compilers



• Early computers (and compilers) were weak

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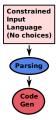
Early compilers



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- Parsing and code generation dominated compilation

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Early compilers



- Early computers (and compilers) were weak
- Parsing and code generation dominated compilation
- Needed a constrained input language to simplify compilation

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- Current computers are much more powerful
- Compilers can do a lot more

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 - Algorithmic choice
 - Iteration order choice
 - Parallelism strategy choice
 - Data layout choice

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- Compilers can do a lot more
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- Input language specifies only one
 - Algorithmic choice
 - Iteration order choice
 - Parallelism strategy choice
 - Data layout choice
- Compiler must perform heroic analysis to reconstruct other choices

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PetaBricks compiler



• We propose explicit choices in the language

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PetaBricks compiler



- We propose explicit choices in the language
- The programmer defines the space of legal
 - Algorithmic choices
 - Iteration orders (include parallel)
 - Data layouts

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PetaBricks compiler



- We propose explicit choices in the language
- The programmer defines the space of legal
 - Algorithmic choices
 - Iteration orders (include parallel)
 - Data layouts
- Allow compilers to focus on exploring choices
- Compiler no longer needs to reconstruct choices

Why choices

Future-proof programs

• The result: programs can adapt to their environment

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Why choices

Future-proof programs

- The result: programs can adapt to their environment
- Choices make programs less brittle

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Why choices

Future-proof programs

- The result: programs can adapt to their environment
- Choices make programs less brittle
- Programs change with architecture, available cores, inputs, etc

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• Algorithmic choice is the key aspect of PetaBricks

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Image: A math a math

- Algorithmic choice is the key aspect of PetaBricks
- Programmer can define multiple rules to compute the same data

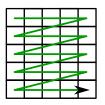
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- Algorithmic choice is the key aspect of PetaBricks
- Programmer can define multiple rules to compute the same data
- Compiler re-use rules to create hybrid algorithms
- Can express choices at many different granularities

• Outer control flow synthesized by compiler

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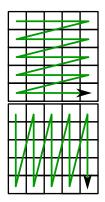
- Outer control flow synthesized by compiler
- Another choice that the programmer should not make



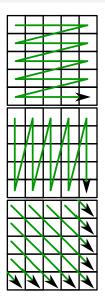
- Outer control flow synthesized by compiler
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 - By rows?

Key Ideas

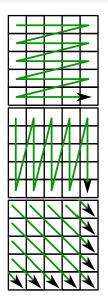
Synthesized outer control flow



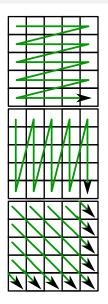
- Outer control flow synthesized by compiler
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- Outer control flow synthesized by compiler
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 - Parallel?



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- Outer control flow synthesized by compiler
- Another choice that the programmer should not make
 - By rows?
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 - Diagonal? Reverse order? Blocked?
 - Parallel?
- Instead programmer provides explicit producer-consumer relations
- Allows compiler to explore choice space

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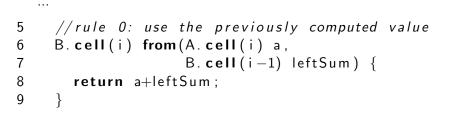
Simple example program

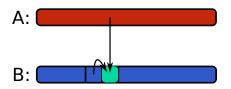
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   to B[n]
3
4
   {
5
   //rule 0: use the previously computed value
6
     B. cell(i) from(A. cell(i) a,
7
                      B. cell (i-1) leftSum \}
8
       return a+leftSum:
9
     }
10
11
   //rule 1: sum all elements to the left
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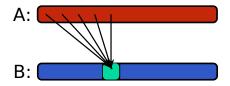


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Applicable regions

Compilation Process		
Applicable regions	Choice grids	Choice dependency graph

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Applicable regions

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Applicable regions	Choice grids	Choice dependency graph

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• Divide data space into symbolic regions with common sets of choices

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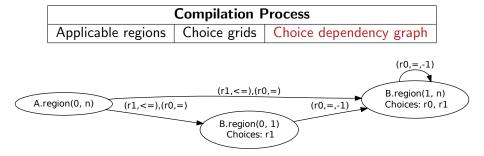
- Divide data space into symbolic regions with common sets of choices
- In this simple example:
 - A: Input (no choices)

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Compilation Process		
Applicable regions	Choice grids	Choice dependency graph

- Divide data space into symbolic regions with common sets of choices
- In this simple example:
 - A: Input (no choices)
 - B: $[0,1) = \mathsf{rule} \ 1$

- Applicable regions map rules \rightarrow symbolic data
- \bullet Choice grids map symbolic data \rightarrow rules

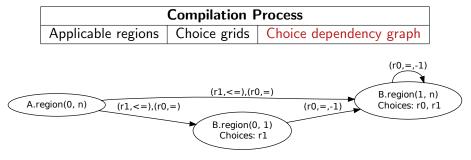


Jason Ansel	(MIT)
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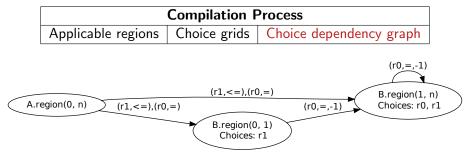


• Adds dependency edges between symbolic regions

Jason Ansel (MIT)

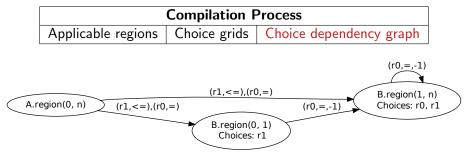
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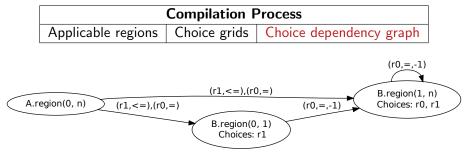
- Adds dependency edges between symbolic regions
- Edges annotated with directions and rules

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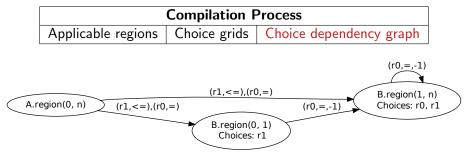
- Adds dependency edges between symbolic regions
- Edges annotated with directions and rules
- Many compiler passes on this IR to:

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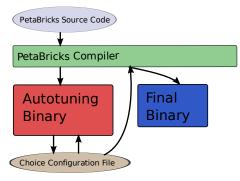


- Adds dependency edges between symbolic regions
- Edges annotated with directions and rules
- Many compiler passes on this IR to:
 - Simplify complex dependency patterns

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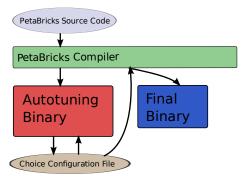
- Adds dependency edges between symbolic regions
- Edges annotated with directions and rules
- Many compiler passes on this IR to:
 - Simplify complex dependency patterns
 - Add choices



PetaBricks source code is compiled

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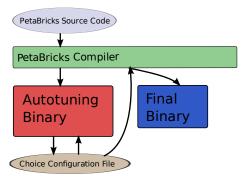
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- PetaBricks source code is compiled
- An autotuning binary is created

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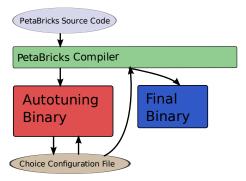
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- PetaBricks source code is compiled
- An autotuning binary is created
- Autotuning occurs creating a choice configuration file

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- PetaBricks source code is compiled
- An autotuning binary is created
- Autotuning occurs creating a choice configuration file
- Choices are fed back into the compiler to create a final binary

Autotuning

- Based on two building blocks:
 - A genetic tuner
 - An *n*-ary search algorithm

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Autotuning

- Based on two building blocks:
 - A genetic tuner
 - An *n*-ary search algorithm
- Flat parameter space
- Compiler generates a dependency graph describing this parameter space

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Autotuning

- Based on two building blocks:
 - A genetic tuner
 - An *n*-ary search algorithm
- Flat parameter space
- Compiler generates a dependency graph describing this parameter space
- Entire program tuned from bottom up

Parallel Runtime Library

- Task-based parallel runtime
- Thread-local decks of runnable tasks

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Image: A math a math

Parallel Runtime Library

- Task-based parallel runtime
- Thread-local decks of runnable tasks
- Use a work-stealing algorithm similar to that of Cilk

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Image: A match a ma

Outline

Introduction

- Motivating Example
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- Why choices

PetaBricks Language

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 - Benchmarks
 - Scalability
 - Variable Accuracy
- Conclusion
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- Automatic consistency checking
- The *tunable* keyword
- Call external code
- Custom training data generators
- Matrix versions for iterative algorithms
- Rule priorities
- where (clause for limiting applicable regions)
- Template transforms

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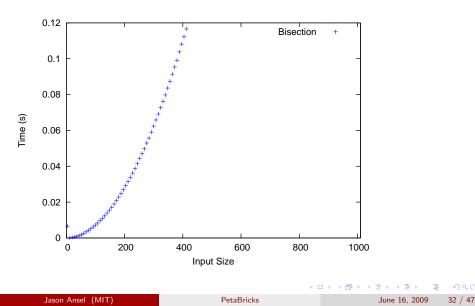
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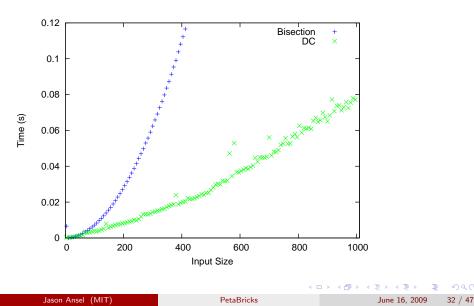
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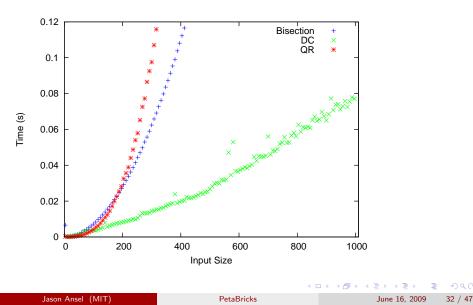
- Bisection
- QR decomposition
- Divide and conquer

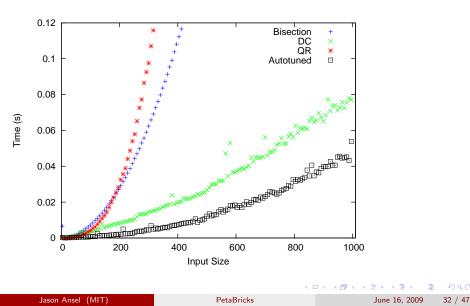
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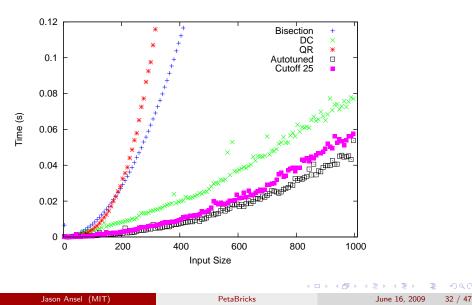






Results

Benchmarks



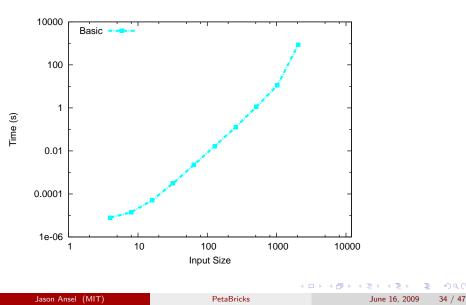
Matrix Multiply

- Basic
- Recursive decompositions
- Strassen's algorithm
- Iteration order (blocking)
- Transpose

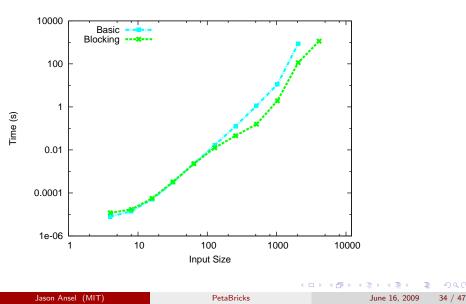
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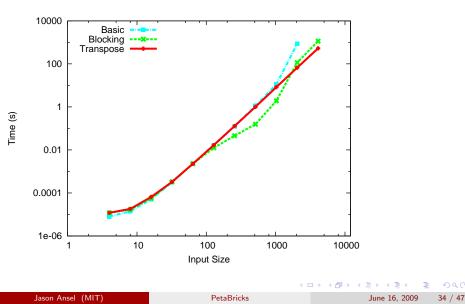
Image: A math a math



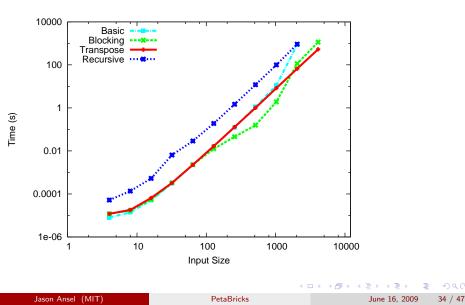
Benchmarks



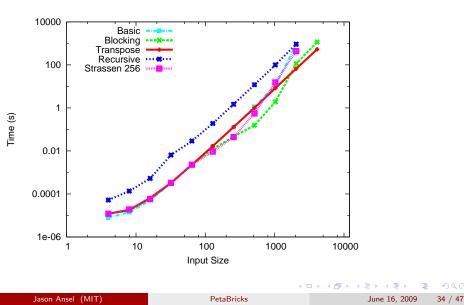
Benchmarks



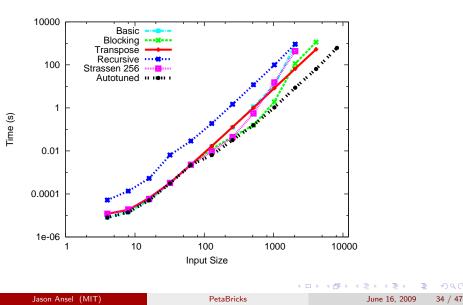
Benchmarks



Benchmarks



Benchmarks



Scalability

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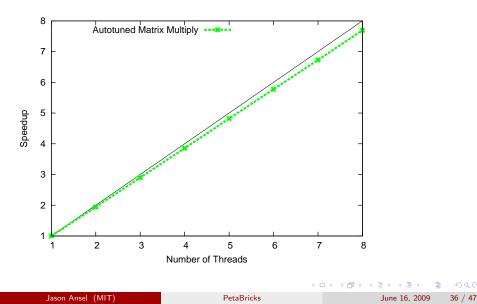
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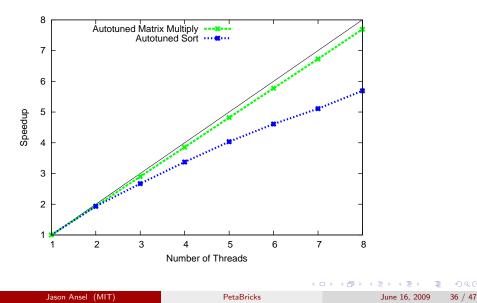
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Scalability

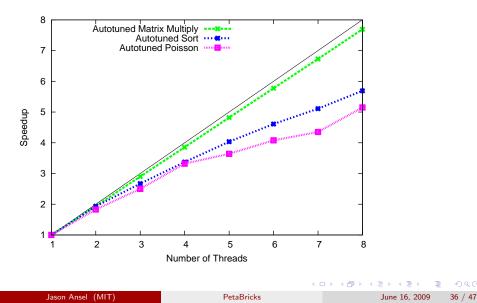




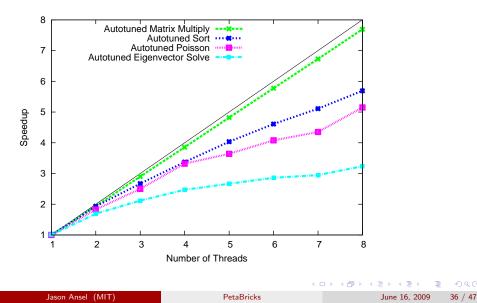
Scalability



Scalability



Scalability



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• Most algorithms produce exact solutions

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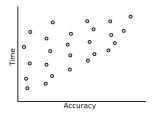
- Most algorithms produce exact solutions
- Large class of algorithms can produce approximate solutions

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- Most algorithms produce exact solutions
- Large class of algorithms can produce approximate solutions
 - Iterative convergence
 - Grid coarsening
 - Others

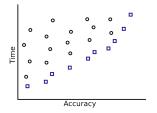
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Variable accuracy



- Most algorithms produce exact solutions
- Large class of algorithms can produce approximate solutions
 - Iterative convergence
 - Grid coarsening
 - Others
- Compiler/autotuner should be aware of variable accuracy

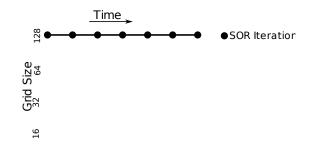
Variable accuracy



- Most algorithms produce exact solutions
- Large class of algorithms can produce approximate solutions
 - Iterative convergence
 - Grid coarsening
 - Others
- Compiler/autotuner should be aware of variable accuracy
- Compiler can examine optimal frontier of algorithms

Poisson's equation

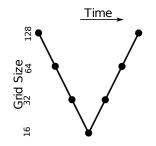
- A variable accuracy benchmark
- Accuracy level expressed as a template parameter
- Autotuner exploits variable accuracy in a general way
- Choices:
 - Direct solve
 - Jacobi iteration
 - Successive over relaxation
 - Multigrid



• SOR is an iterative algorithm

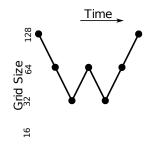
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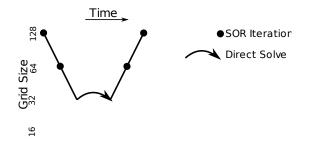
SOR Iteratior

- SOR is an iterative algorithm
- Multigrid changes grid coarseness to speed up convergence
- Many standard shapes: V-Cycle,

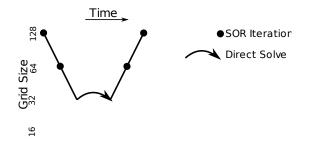


SOR Iteratior

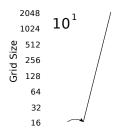
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- SOR is an iterative algorithm
- Multigrid changes grid coarseness to speed up convergence
- Many standard shapes: V-Cycle, W-Cycle, etc
- Direct solver

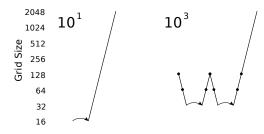


- SOR is an iterative algorithm
- Multigrid changes grid coarseness to speed up convergence
- Many standard shapes: V-Cycle, W-Cycle, etc
- Direct solver
- Different shapes = different algorithms



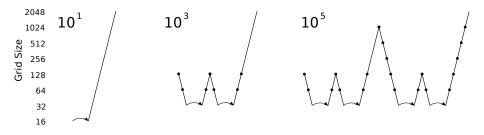
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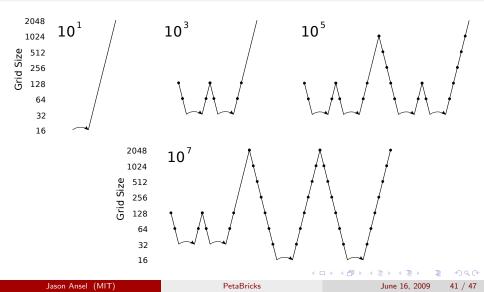
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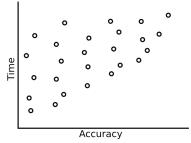
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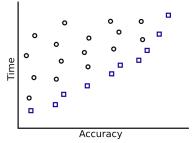




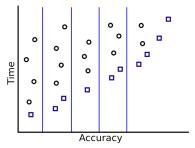
Dynamic programming technique for autotuning Multigrid



Dynamic programming technique for autotuning Multigrid



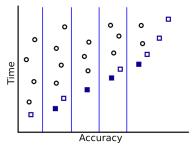
Dynamic programming technique for autotuning Multigrid



• Partition accuracy space into discrete levels

Jason Ansel	(MIT)	١

Dynamic programming technique for autotuning Multigrid



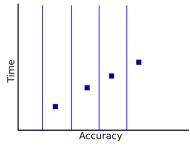
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Variable Accuracy

Dynamic programming technique for autotuning Multigrid



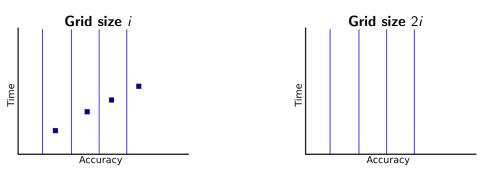
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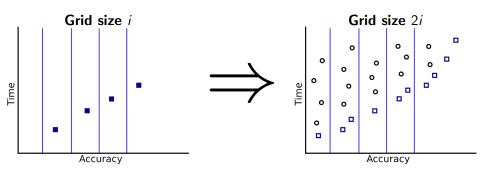
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Jason	Ansel ((MIT))

Results Va

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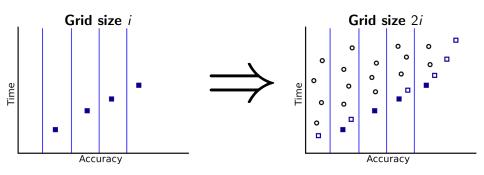
- Partition accuracy space into discrete levels
- Base space of candidate algorithms on optimal algorithms from coarser level

Jason Ansel	(MIT))

Results Var

Variable Accuracy

Dynamic programming technique for autotuning Multigrid



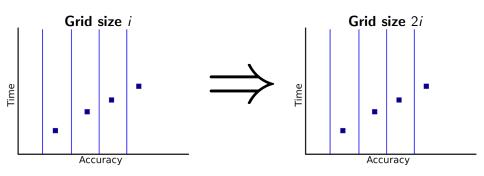
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Results Varial

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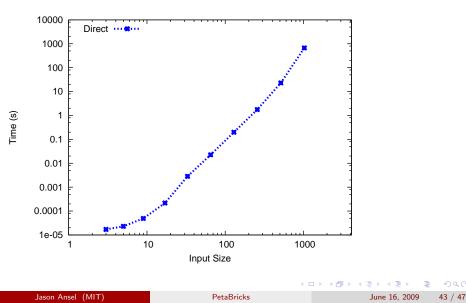
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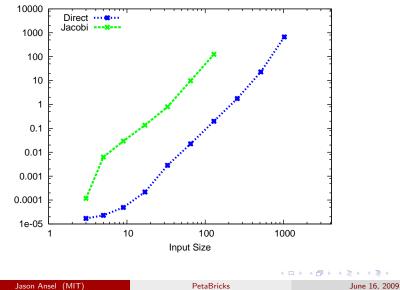
Jason Ansel	(MIT))

Poisson's Equation



43 / 47

Poisson's Equation

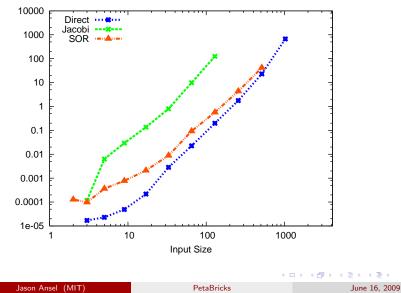


Time (s)

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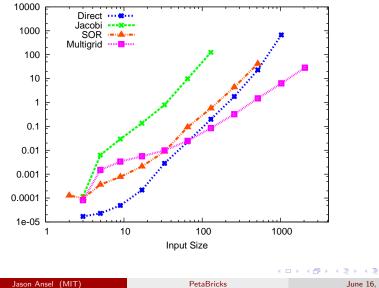
Poisson's Equation



Time (s)

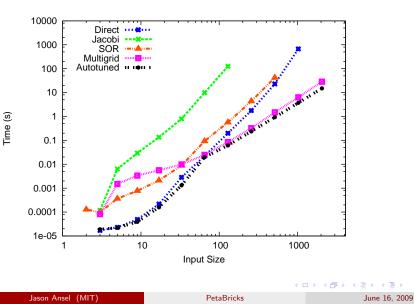
Poisson's Equation

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Poisson's Equation



Final thoughts

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Related work

Languages

• Sequoia

• Libraries & domain specific tuners

- STAPL
- ATLAS
- FFTW
- SPARSITY
- SPIRAL
- ...

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For more information

- PetaBricks makes programs **future-proof**, by allowing them to adapt to new architectures
- We plan to released PetaBricks at the end of summer
- Sign up for our mailing list to be notified
- For more information see: http://projects.csail.mit.edu/petabricks/
- Questions?

Thank you!

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